## Purpose

Team AND has been tasked with designing and implementing a Digital Signal Processor concept circuit block. This document serves to outline the team progress and to document the remaining tasks in the project.

## Progress

### Completed Tasks:

* Successfully adapted team wiki page to show the team members, group email address and project schedule
* Conducted first team meeting where tasks for Design Review 1 were divided up.
* Developed schematics for the AND, OR, PASS A and 8:1 MUX components
* Simulated the functionality of the AND, OR, PASS A and 8:1 MUX components
* Created a block diagram of the ALU block in the DSP using the Logisim Circuit Design Software

### To Be Completed Prior to Design Review II:

* Set a schedule for weekly group meetings
* Decide which “Arbitrary” function we would like our DSP to implement
* Consult with Prof. Calhoun about Arbitrary function
* Draft schematics for the ADD, SUB, SHIFT, in/out connectivity and Arbitrary circuit functions
* Simulate the functionality of the ADD, SUB, SHIFT , in/out connectivity, registers and Arbitrary circuit functions
* Create a block diagram of the entire DSP system
* Document progress in comprehensive Progress Report

### To Be Completed After Design Review II:

* Simulate and test entire circuit design
* Verify that all parts are functioning correctly
* Draft final report
* Create final presentation (Slide Show)

## Arbitrary Function Ideas

When thinking about which arbitrary function to implement, we came up with many ideas. Although they seem valid, our team plans on conferring with Professor Calhoun before making our final selection. Our list of possibilities is as follows: multiplication, division, squaring, powers, square root, quadratic formula, Fibonacci Sequence, derivatives, integrals, natural log, exponential, permutations/combinations.